



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN FEBRUARY 2022
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3101 P R No : 201702184 Sex : F Name : COLACO LINDSEY VELANY
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	AA P	
	IA	2	AO P	
Digital VLSI Design	Theory	4	AA P	
	IA	2	AB P	
Analog VLSI Design	Theory	4	BC P	
	IA	2	AO P	
VLSI Technology	Theory	4	AA P	
	IA	2	AB P	
Advanced Engineering Mathematics	Theory	4	BC P	
	IA	2	AB P	
HDL - LAB	IA	2	AA P	
	Practical	2	AA P	
VLSI CAD - LAB	IA	2	AB P	
	Practical	2	AB P	
Total :		38		8.21 P PASSES

Seat No : 3102 P R No : 201707810 Sex : F Name : JAINAPUR SHILPA BHIMAPPA
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	CC P	
	IA	2	AA P	
Digital VLSI Design	Theory	4	FF F	
	IA	2	AB P	
Analog VLSI Design	Theory	4	FF F	
	IA	2	AA P	
VLSI Technology	Theory	4	BB P	
	IA	2	BB P	
Advanced Engineering Mathematics	Theory	4	CC P	
	IA	2	AB P	
HDL - LAB	IA	2	CC P	
	Practical	2	CC P	
VLSI CAD - LAB	IA	2	BB P	
	Practical	2	AB P	
Total :		38		5.26 F FAILS



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COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3103 P R No : 201701064 Sex : M Name : PARAB PRAJWAL BHARAT
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	AB P	
	IA	2	AA P	
Digital VLSI Design	Theory	4	BB P	
	IA	2	AB P	
Analog VLSI Design	Theory	4	CC P	
	IA	2	AB P	
VLSI Technology	Theory	4	AB P	
	IA	2	BB P	
Advanced Engineering Mathematics	Theory	4	FF F	
	IA	2	BB P	
HDL - LAB	IA	2	BB P	
	Practical	2	BC P	
VLSI CAD - LAB	IA	2	AB P	
	Practical	2	AA P	
Total :		38		6.58 F FAILS

Seat No : 3104 P R No : 201608095 Sex : F Name : PRAJACTA NAVANATH NAIK
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	AA P	
	IA	2	AA P	
Digital VLSI Design	Theory	4	AB P	
	IA	2	AA P	
Analog VLSI Design	Theory	4	BC P	
	IA	2	AA P	
VLSI Technology	Theory	4	AB P	
	IA	2	AA P	
Advanced Engineering Mathematics	Theory	4	BC P	
	IA	2	AB P	
HDL - LAB	IA	2	BB P	
	Practical	2	BB P	
VLSI CAD - LAB	IA	2	AA P	
	Practical	2	AB P	
Total :		38		7.84 P PASSES



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COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3105	P R No : 201608365	Sex : M	Name : SANISH GURUDAS NAIK	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	CC P	
	IA	2	BB P	
Digital VLSI Design				
	Theory	4	BC P	
	IA	2	AB P	
Analog VLSI Design				
	Theory	4	FF F	
	IA	2	AA P	
VLSI Technology				
	Theory	4	CC P	
	IA	2	BB P	
Advanced Engineering Mathematics				
	Theory	4	FF F	
	IA	2	CC P	
HDL - LAB				
	IA	2	AB P	
	Practical	2	BB P	
VLSI CAD - LAB				
	IA	2	AB P	
	Practical	2	AA P	
Total :		38		5.26 F FAILS

Seat No : 3106	P R No : 201607806	Sex : M	Name : VAIGANKAR EAISHWARYA SAIHEEL SANJAY	
No Of Attempts : 1		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics				
	Theory	4	CC P	
	IA	2	BB P	
Digital VLSI Design				
	Theory	4	CC P	
	IA	2	BC P	
Analog VLSI Design				
	Theory	4	FF F	
	IA	2	AB P	
VLSI Technology				
	Theory	4	FF F	
	IA	2	BB P	
Advanced Engineering Mathematics				
	Theory	4	FF F	
	IA	2	BB P	
HDL - LAB				
	IA	2	CC P	
	Practical	2	CC P	
VLSI CAD - LAB				
	IA	2	AB P	
	Practical	2	AB P	
Total :		38		4.26 F FAILS



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HELD IN FEBRUARY 2022
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COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3107 P R No : 201408239 Sex : F Name : VAZE RUCHI RAMCHANDRA
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics			
Theory	4	AO P	
IA	2	AO P	
Digital VLSI Design			
Theory	4	AA P	
IA	2	AA P	
Analog VLSI Design			
Theory	4	BB P	
IA	2	AA P	
VLSI Technology			
Theory	4	AA P	
IA	2	AA P	
Advanced Engineering Mathematics			
Theory	4	BC P	
IA	2	AB P	
HDL - LAB			
IA	2	AA P	
Practical	2	AA P	
VLSI CAD - LAB			
IA	2	AO P	
Practical	2	AA P	
Total :	38		8.63 P PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *[Signature]*
Checked By : *[Signature]*
Date : 02/06/2022

[Signature] Assistant Registrar-E(Prof.)
[Signature] Assistant Registrar Exam
[Signature] Controller Of Examinations

